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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/601,852	06/24/2003	Yasunobu Nakase	239268US2	8120
22850	22850 7590 09/07/2004		EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.			NGUYEN, TAN	
	1940 DUKE STREET ALEXANDRIA, VA 22314		ART UNIT	PAPER NUMBER
			2818	
			DATE MAIL ED: 09/07/2004	1

Please find below and/or attached an Office communication concerning this application or proceeding.

			130
	Application No.	Applicant(s)	
	10/601,852	NAKASE, YASUNOBU	
Office Action Summary	Examiner	Art Unit	
	Tan T. Nguyen	2818	
The MAILING DATE of this communicati Period for Reply	on appears on the cover sheet	with the correspondence address	
A SHORTENED STATUTORY PERIOD FOR THE MAILING DATE OF THIS COMMUNICAT - Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailling date of this communica - If the period for reply specified above is less than thirty (30) day - If NO period for reply is specified above, the maximum statutory - Failure to reply within the set or extended period for reply will, be Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	CFR 1.136(a). In no event, however, may tion. is, a reply within the statutory minimum of ty period will apply and will expire SIX (6) May statute, cause the application to become	a reply be timely filed hirty (30) days will be considered timely. ONTHS from the mailing date of this communi ABANDONED (35 U.S.C. § 133).	cation.
Status			
1) Responsive to communication(s) filed or) .	·	
	This action is non-final.		
3) Since this application is in condition for a	_	atters, prosecution as to the meri	its is
closed in accordance with the practice u	· · · · · · · · · · · · · · · · · · ·	· ·	
Disposition of Claims		· · · · · · · · · · · · · · · · · · ·	
4) ☐ Claim(s) 1-23 is/are pending in the application 4a) Of the above claim(s) is/are w 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1 is/are rejected. 7) ☐ Claim(s) 2-23 is/are objected to. 8) ☐ Claim(s) are subject to restriction	ithdrawn from consideration.		
Application Papers			
9)☐ The specification is objected to by the Ex	aminer.		
10) The drawing(s) filed on is/are: a)[☐ accepted or b) ☐ objected t	o by the Examiner.	
Applicant may not request that any objection	to the drawing(s) be held in abey	ance. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the 11) The oath or declaration is objected to by	·	= , , ,	• •
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for fa a) All b) Some * c) None of: 1. Certified copies of the priority doct 2. Certified copies of the priority doct 3. Copies of the certified copies of the application from the International I * See the attached detailed Office action for	uments have been received. uments have been received in e priority documents have bee Bureau (PCT Rule 17.2(a)).	Application No en received in this National Stage	Э
Attachment(s)			
1) Notice of References Cited (PTO-892)		v Summary (PTO-413)	
 Notice of Draftsperson's Patent Drawing Review (PTO-93) Information Disclosure Statement(s) (PTO-1449 or PTO-Paper No(s)/Mail Date 06/24/03. 		o(s)/Mail Date f Informal Patent Application (PTO-152) 	

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1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

- 2. The Information Disclosure Statement submitted by Applicant on June 24, 2004 has been received and fully considered.
- 3. Claims 20-23 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 20 recites the limitation "said AND circuit" in line 5. There is insufficient antecedent basis for this limitation in the claim.

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 5. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Price (U.S. Patent No. 4,758,982).

Price disclosed in Figure 1 a quasi content addressable memory having a RAM section [14] which includes r memory locations and storing content RAM fields TR (column 4, lines 14-16), and a comparator [16] which compares comparand RAM field PR which is input on a port A to content RAM field TR which is input on a port [B]. Comparator [16] is enabled by signal [VALPTR] on line [24]. The RAM section [14] would be understood as the claimed storage unit, the content RAM field TR from the RAM section [14] would be understood as the claimed first multi-bit data, the comparand RAM field [PR] on a

second portion [22] of the comparand bus [18] (column 3, lines 54-57) would be understood as the claimed second multi-bit data, and the signal [VALPTR] would be understood as the claimed first control signal. Although Price did not discuss the content RAM field TR and the comparand PR are multi-bit data, it is inherent that these are multi-bit data since they are "fields" in the memory circuit.

6. Claims 2-19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art did not show or suggest the sense amplifier outputting a first activation signal, an active state of the said sense amplifier is controlled by the first control signal, and the comparator is controlled based on the first activation signal outputted from the sense amplifier as claimed in claims 2-19. The prior art also did not show or suggest the comparator includes a plurality of exclusive OR and a AND circuit as claimed in claims 20-23.

- 7. Claims 20-23 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.
- 8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Zaiki, Maguire, Simcoe, McElroy, Lysinger are cited to show memory devices having comparator circuit.

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9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tan T. Nguyen whose telephone number is (571) 272-1789. The examiner can normally be reached on Monday to Friday from 07:00 AM to 03:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms, can be reached at (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tan T. Nguyen
Primary Examiner
Art Unit 2818
September 02, 2004